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TECHNICAL

PATENT NUMBER and
ISSUE DATE

1

U.S. UTILITY Patent Application

APPL NUM 10038734	FILING DATE 12/31/2001	CLASS 438	SUBCLASS 50	GAU 2812	EXAMINER Booth
**APPLICANTS: Choi Seungmoo; Cwynar Donald; Shive Scott; Doyle Timothy; Llevada Felix;					
BEST AVAILABLE COPY					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A DIV OF 09/523,782 03/13/2000 <div style="text-align: right;">438 2812 LUT 2 0039-2</div>					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO LUT 2 0039-2	
Verified and Acknowledged Examiners's initials					
TITLE : Method for fabricating MOS device with halo implanted region					
<small>U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)</small>					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED			
		Assistant Examiner	Total Claims	Print Claim for O.G.	
ISSUE FEE			DRAWING		
Amount Due	Date Paid	Primary Examiner	Sheets Drawg.	Figs. Drawg.	Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner		
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